

Notice of References Cited	Application/Control No. 10/600,715	Applicant(s)/Patent Under Reexamination SHARMA, VINOD	
	Examiner Stephen Elmore	Art Unit 2186	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,649,139	07-1997	Weinreb et al.	711/202
*	B	US-6,223,260	04-2001	Gujral et al.	711/145
*	C	US-5,813,034	09-1998	Castle et al.	711/146
*	D	US-5,802,578	09-1998	Lovett, Thomas D.	711/147
*	E	US-5,796,980	08-1998	Bowles, James E.	711/144
*	F	US-5,699,551	12-1997	Taylor et al.	711/207
*	G	US-5,623,627	04-1997	Witt, David B.	711/122
*	H	US-4,442,487	04-1984	Fletcher et al.	711/122
*	I	US-6,122,709	09-2000	Wicki et al.	711/118
*	J	US-5,506,967	04-1996	Barajas et al.	711/118
*	K	US-5,553,263	09-1996	Kalish et al.	711/127
*	L	US-5,692,152	11-1997	Cohen et al.	711/140
	M	US-6,591,341	07-2003	Sharma, Vinod	711/122

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Lee et al., "Shared tag for MMU and cache memory," pp 77-80, CAS '97 Proceedings, Vol. 1, IEEE, October 1997.
*	V	Lee et al., "Indirectly-compared cache tag memory using a shared tag in a TLB," pp 1764-1766, Electronics Letters, Vol. 3, No. 21, October 1997.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.